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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FEB 0 8 2006

in ke Application of:		) }
Inventors:	Ono et al.	) ATTORNEY FILE NO.: SLA0830
Serial No.:	10/805,158	) ) ) Examiner: Pizarro-Crespo \
Filed:	March 19, 2004	) Customer No.: 55,286
Title:	CHARGE TRAP NON- VOLATILE MEMORY STRUCTURE FOR 2 BITS PER TRANSISTOR	) Group Art: 2814 ) Confirmation No.: 8642

Mail Stop Amendments Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## **AMENDMENT UNDER 37 CFR 1.115**

In response to an Office Action filed November 29, 2005, please amend and reconsider the above-referenced application as follows.

Amendments to the claims begin at page 2 of this paper.

Remarks begin at page 5 of this paper.